

10/829,527

Substitute for Form 1449A/PTO (Modified) (use as many sheets as necessary)			Attorney Docket No.: 001207.P004XC6		Application Number: <del>Not yet assigned</del>	
			First Named Inventor: Benjamin S. Ting			
			Filing Date: Herewith			
<b>U.S. PATENT DOCUMENTS</b>						
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		Number Code <sup>2</sup>	Kind (If known)			
<i>BT</i>		4,935,734		Austin	6/19/90	
<i>BT</i>		4,992,680		Benedetti, et al.	2/12/91	
<i>BT</i>		5,396,126		Britton, et al.	3/7/95	
<i>BT</i>		5,144,166		Camarota et al.	9/1/92	

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		Office <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>5</sup> (If known)				
<i>BT</i>		PCT	9410754			5/11/94		
		PCT	9208286			5/14/92		
		EPO	0415542			3/6/91		
		UK	2180382			3/25/87		
		EPO	0630115A2			6/1/94		
		PCT	9504404			2/9/95		
		PCT	9605964			4/3/96		
		PCT	9635261			11/7/96		
		PCT	9428475			12/8/94		
<i>BT</i>		UK	2295738			5/6/96		

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✓		5,550,782		Cliff, et al.	8/27/96	
		4,720,780		Dolecek	1/19/88	
		5,208,491		Ebeling, et al.	5/4/93	
		4,870,302		Freeman	9/26/89	
		4,700,187		Furtek	10/13/87	
		4,918,440		Furtek	4/17/90	
		5,298,805		Garverick, et al.	3/29/94	
		5,455,525		Ho, et al.	10/3/95	
		5,477,067		Isomura, et al.	12/19/95	
		5,903,165		Jones, et al.	5/11/99	
		4,847,612		Kaplinsky	7/11/89	
		5,581,767		Katuski, et al.	12/3/96	
✓		5,243,238		Kean	9/7/93	

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<i>JSW</i>		5,469,003		Kean	11/21/95	
		5,552,722		Kean	9/3/96	
		5,572,148		Lytle, et al.	11/5/96	
		4,020,469		Manning	4/26/77	
		4,736,333		Mead, et al.	4/5/88	
		5,406,525		Nicholes	4/11/95	
		5,260,610		Pederson, et al.	11/9/93	
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		5,329,470		Sample, et al.	7/12/94	
		5,204,556		Shankar	4/20/93	
		5,519,629		Snider	5/21/96	
<i>JSW</i>		5,296,759		Sutherland, et al.	3/22/94	

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		Office <sup>3</sup> Code <sup>5</sup>	Number <sup>4</sup>	Kind (If known)				

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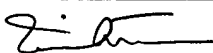
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17th		5,457,410		Ting	10/10/95	
		5,835,405		Tsui, et al.	11/10/98	
		4,661,901		Veneski	4/28/87	
		6,051,991		Ting	4/18/00	
		6,038,627		Plants	3/14/00	
		6,034,547		Pani, et al.	3/7/00	
		4,912,342		Wong, et al.	3/1990	
		Re 34,363		Freeman	8/1993	
		6,160,420		Gamal	12/2000	
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		4,758,745		Elgamal	7/1988	
		5,122,685		Chan	6/1992	
1st		5,187,393		Elgamal et al.	2/16/93	

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✓		5,850,564		Ting et al.	12/1998	
		6,088,526		Ting et al.	7/2000	
		4,815,003		Putatunda et al.	3/21/89	
		5,444,394		Watson, et al.	8/1995	
		5,369,314		Patel, et al.	11/1994	
		5,349,691		Harrison, et al.	9/1994	
		5,485,103		Pederson et al.	1/16/96	
		5,376,844		Pederson et al.	12/1994	
		5,260,611		Cliff et al.	9/1993	
		5,256,918		Suzuki	10/1993	
✓		6,433,580		Ting	8/13/02	

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<b>OTHER ART - NO PATENT LITERATURE DOCUMENTS</b>			
Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	Translation <sup>2</sup>
UT		Cliff, et al., "A Dual Granularity and Globally Interconnected Architecture for A Programmable Logic Device", 9/5/93, IEEE, pp. 7.3.1-7.3.5	
		BRITTON, et al., "Optimized Reconfigurable Cell Array Architecture for High-Performance Field Programmable Gate Arrays," Proceedings of the IEEE 1993 Custom Integrated Circuits Conference, 1993, pp. 7.2.1. - 7.2.5. <i>No mention</i>	
		Xilinx, "The Programmable Gate Array Data Book", 1992. <i>No mention</i>	
		Minnick, R.C., "A Survey of Microcellular Research", vol. 14, no. 2, 4/1967, pp. 203-241.	
		SHOUP, R. G., "Programmable Cellular Logic Arrays," Abstract, Ph.D. Dissertation, Carnegie Mellon University, Pittsburgh, PA, March 1970, (partial) pp. ii-121.	
		SPANDORFER, L.M., "Synthesis of Logic Functions on an Array of Integrated Circuits," Contract No. AF 19 (628) 2907, Project No. 4645, Task No. 464504, Final Report, November 30, 1965.	
		Wang, P. et al. IEEE, "A High Performance FPGA with Hierarchical Interconnection Structure", pp. 239-242 (May 30, 1994).	
		ATMEL Field Programmable Arrays, AT 6000 Series, 1993, p. 1-16. <i>No mention</i>	
		Motorola Project Brief, "MPA10xx Field Programmable Gate Arrays," September 27, 1993, 4 pages.	
UT		Altera Corporation Data Sheet, Flex EPF81188 12,000 Gate Programmable Logic Device, September 1992, Ver. 1, pp. 1-20.	

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UA		Sinan Kaptanoglu, Greg Bakker, Arun Kundu, Ivan Corneillet, Ben Ting, "A New High Density and Very Low Cost Reprogrammable FPGA Architecture", 10 pages, Actel Corporation. <i>No date.</i>	
		BUFFOLI, E., et al., "Dynamically Reconfigurable Devices Used to Implement a Self-Tuning, High Performances PID Controller," 1989 IEEE, pp., 107-112. <i>No month</i>	
		DEVADES, S., et al., "Boolean Decomposition of Programmable Logic Arrays," IEEE 1988, pp. 2.5.1 - 2.5.5. <i>No month</i>	
		VIDAL, J. J., "Implementing Neural Nets with Programmable Logic," IEEE Transactions on Acoustic, Speech, and Signal Processing, Vol. 36, No. 7, July 1988, pp. 1180-1190.	
		LIU, D.L., et al., "Design of Large Embedded CMOS PLA's for Built-In Self-test," IEEE Transactions on Computed-Aided Design, Vol. 7, No. 1, January 1988, pp. 50-53.	
		SUN, Y., et al., "An Area Minimizer for Floorplans with L-Shaped Regions," 1992 International Conference on Computer Design, 1992 IEEE, pp. 383-386. <i>No month</i>	
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		F. Zlotnick, P. Butler, W. Li, D. Tang, "A High Performance Fine-Grained Approach to SRAM Based FPGAs", p. 321-326, Wescon September 28-30, 1993.	
UA		Robert H. Krambeck, "ORCA: A High Performance, Easy to Use SRAM Based Architecture", p. 310-320, Wescon September 28-30, 1993.	

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